

Abstract of the Disclosure:

A circuit configuration has a first driver stage for feeding in an input signal and for outputting an amplified signal. A second driver stage, which is connected in parallel with the first driver stage, is fed, on the input side, both the input
5 signal and a control signal from a reference circuit connected upstream. The reference circuit compares the feedback level of an output signal, which level is present at one of its inputs, with the level of the input signal present at its
10 other input and generates the control signal for driving the driver stage in the event that the level of the output signal is lower than the level of the input signal. As a result, the driver stage is connected for additional amplification of the input signal.

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